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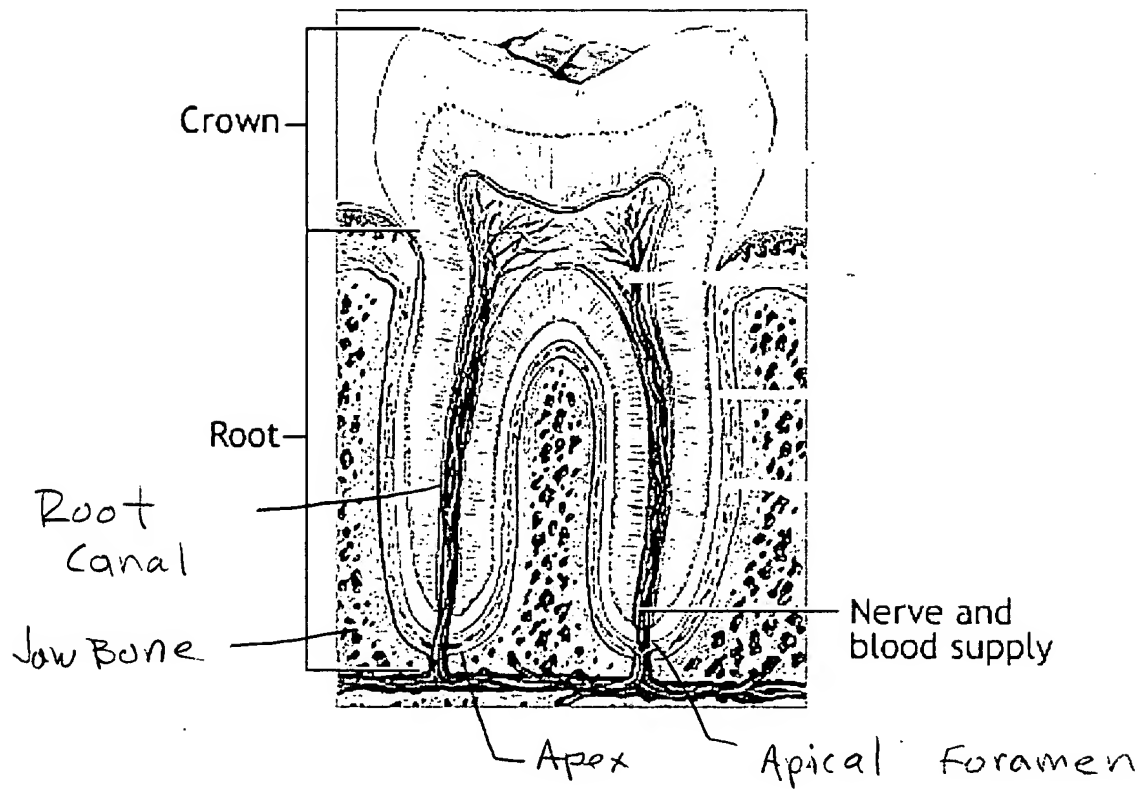


FIG. 1

50 →

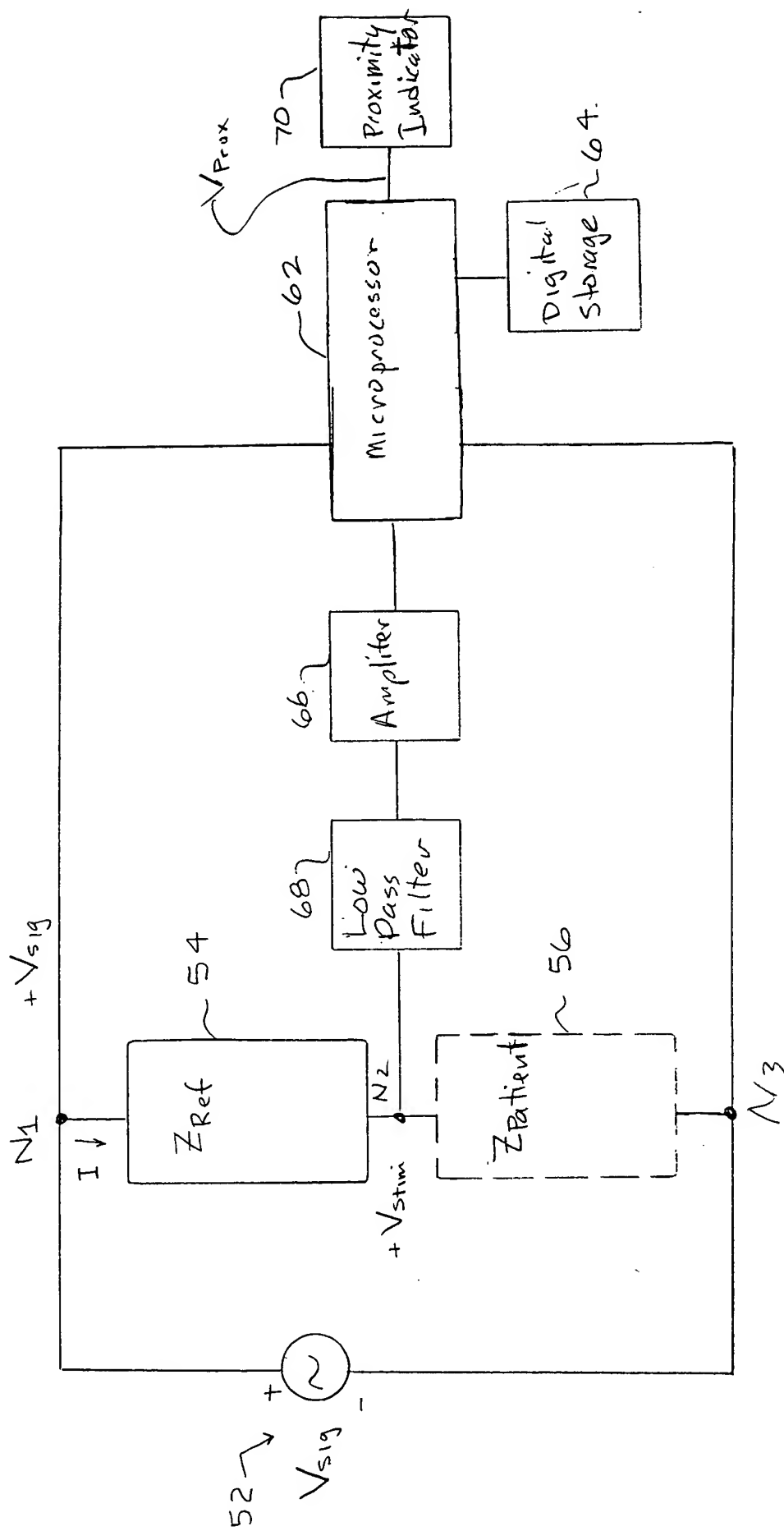
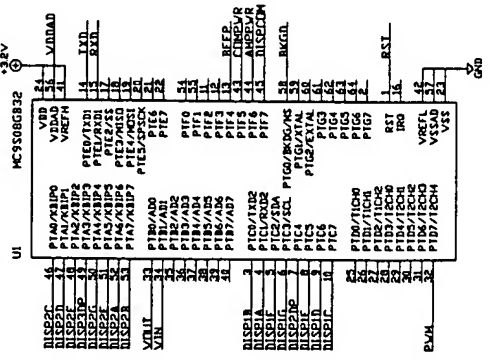


FIG. 2

100

MICROPROCESSOR PIN ASSIGNMENT



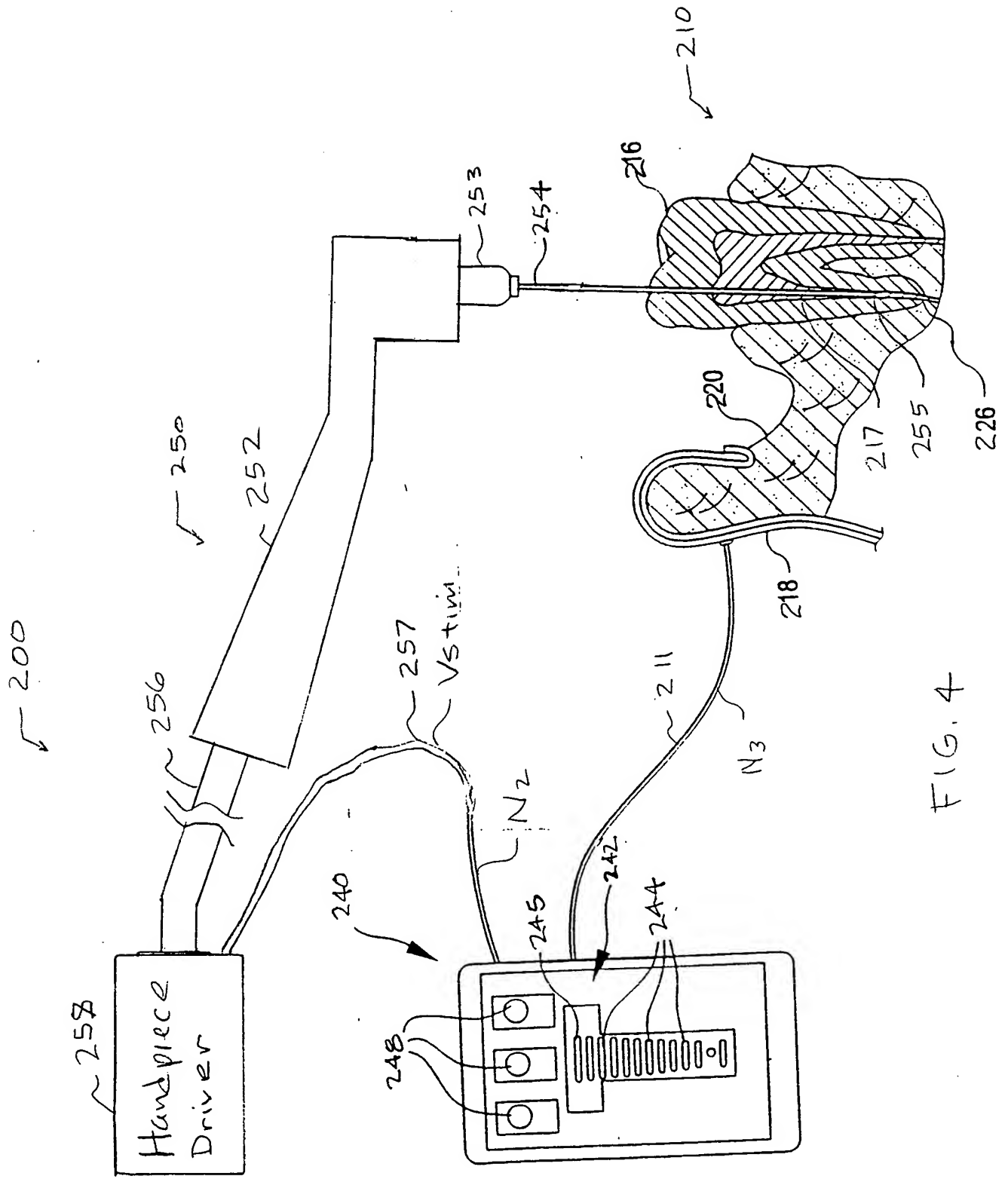


FIG. 4